

Processor Instruction: min rD, rA, rB, index

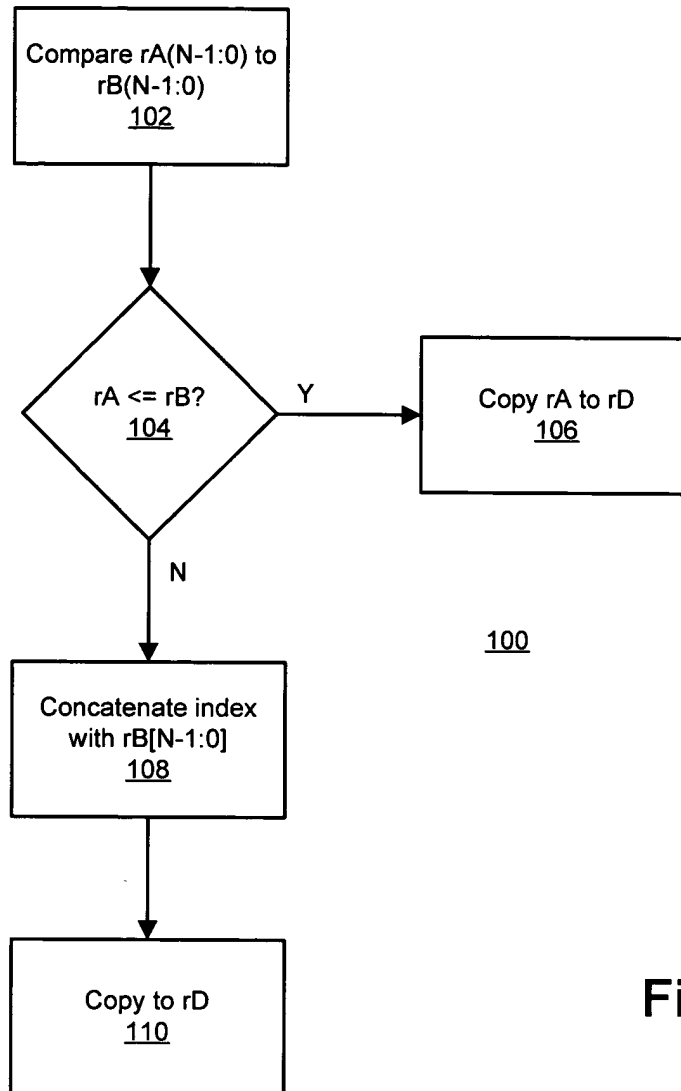
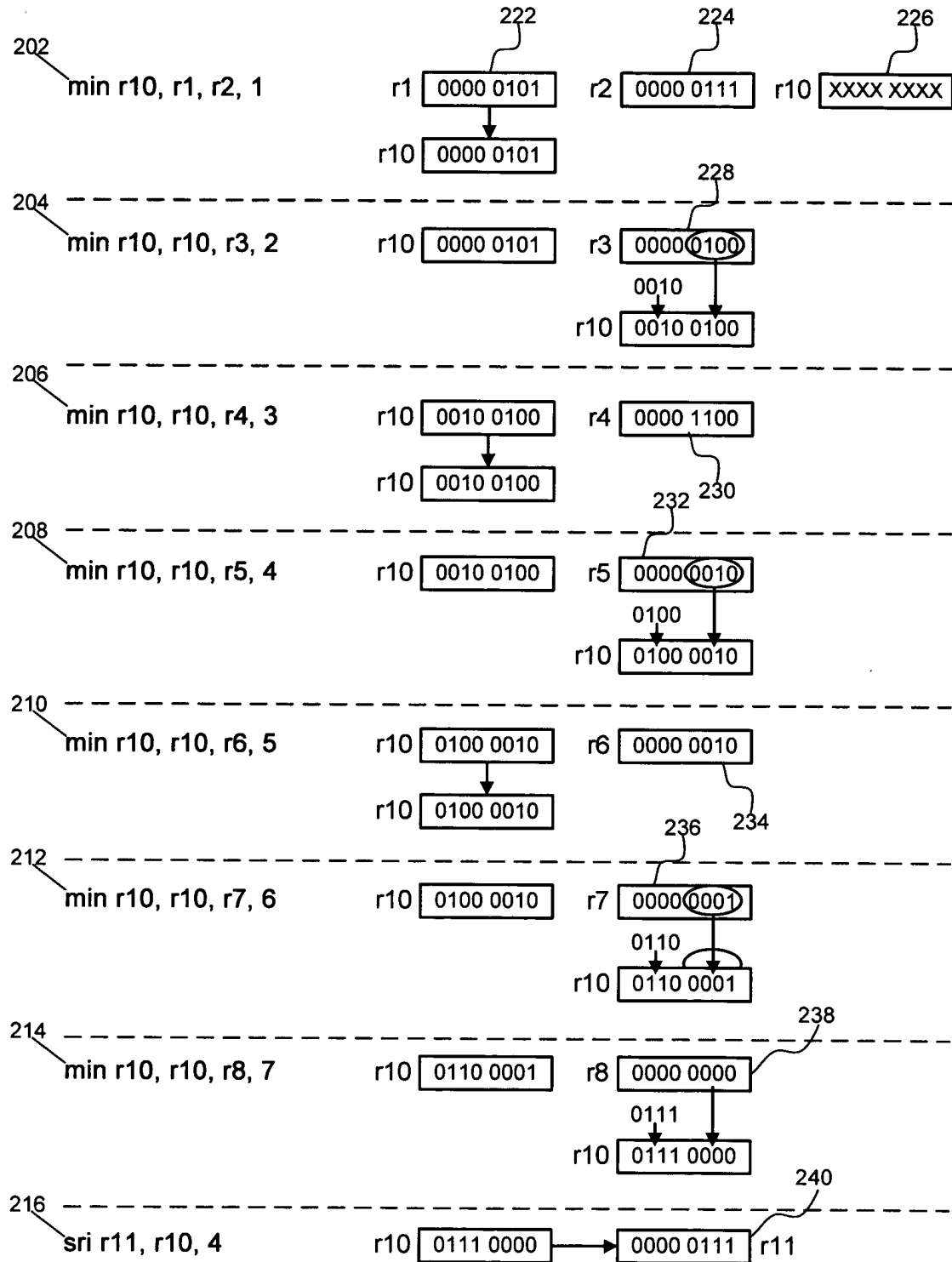


Fig. 1

Fig. 2



Processor Instruction: Min rD, rA, rB, index

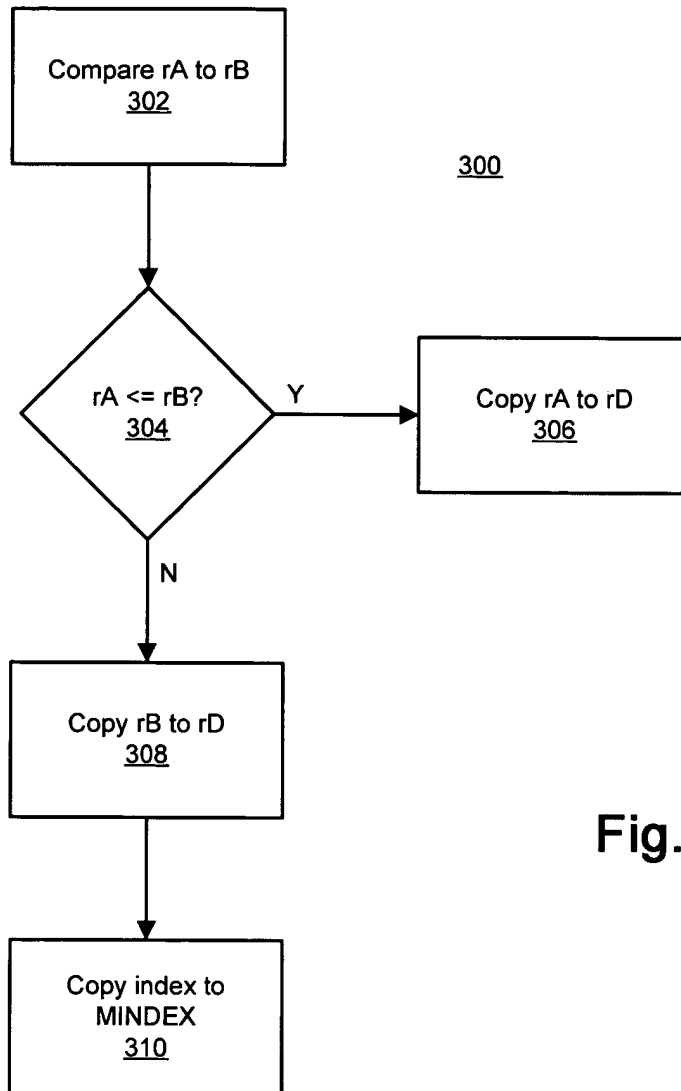


Fig. 3

Fig. 4

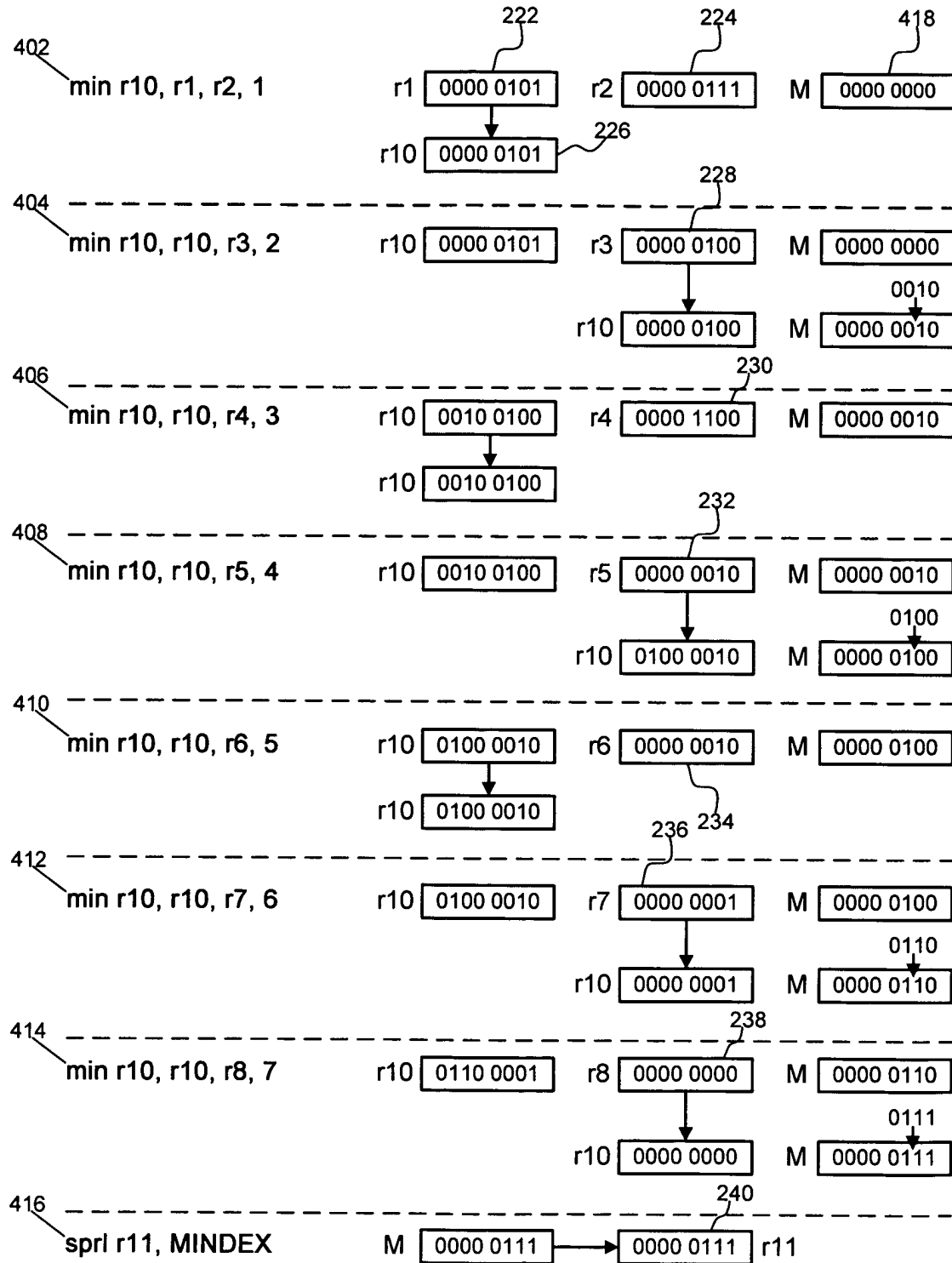


Fig. 5

Processor Instruction: MIN rD, rA, rB, index

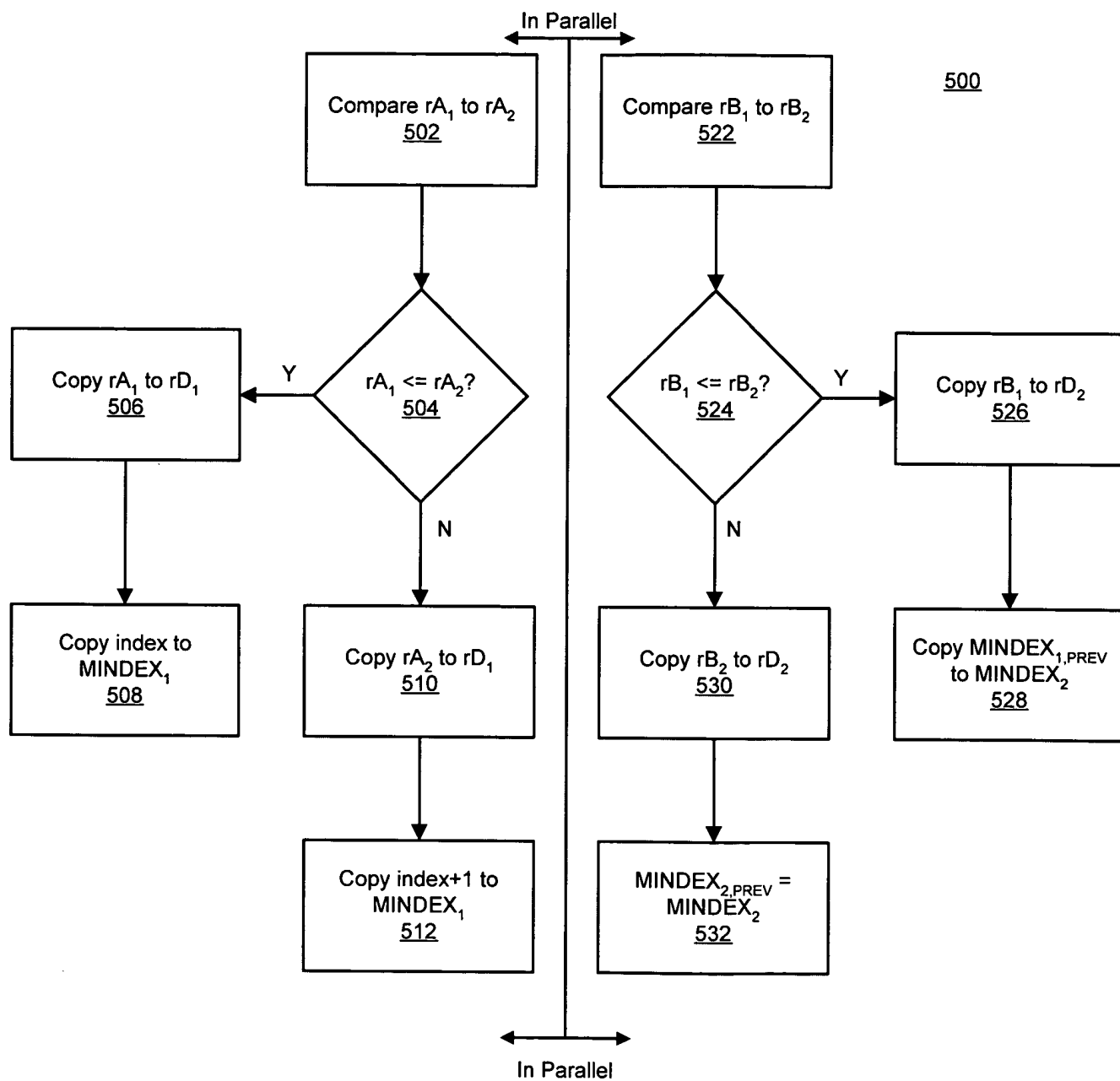
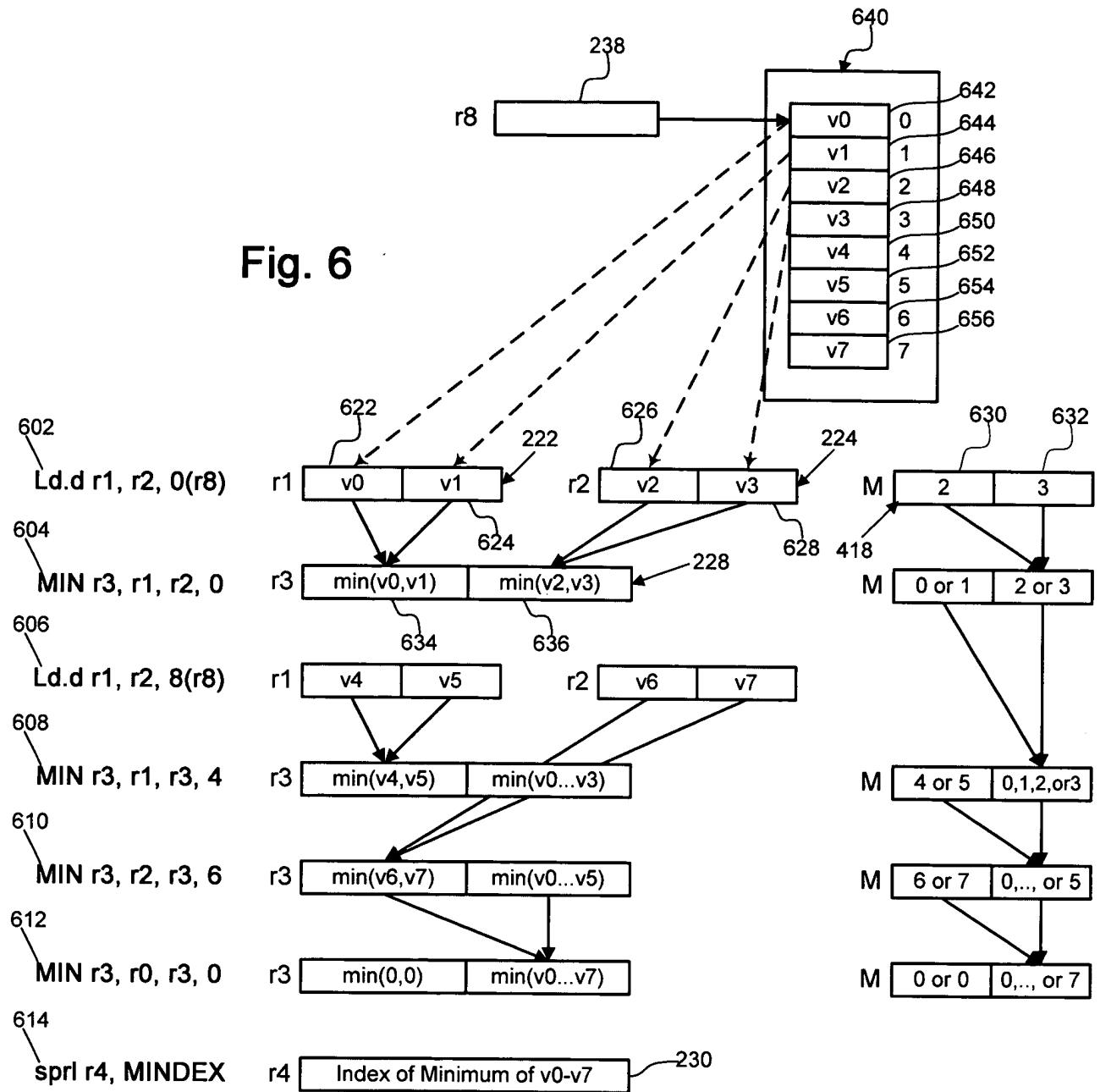


Fig. 6



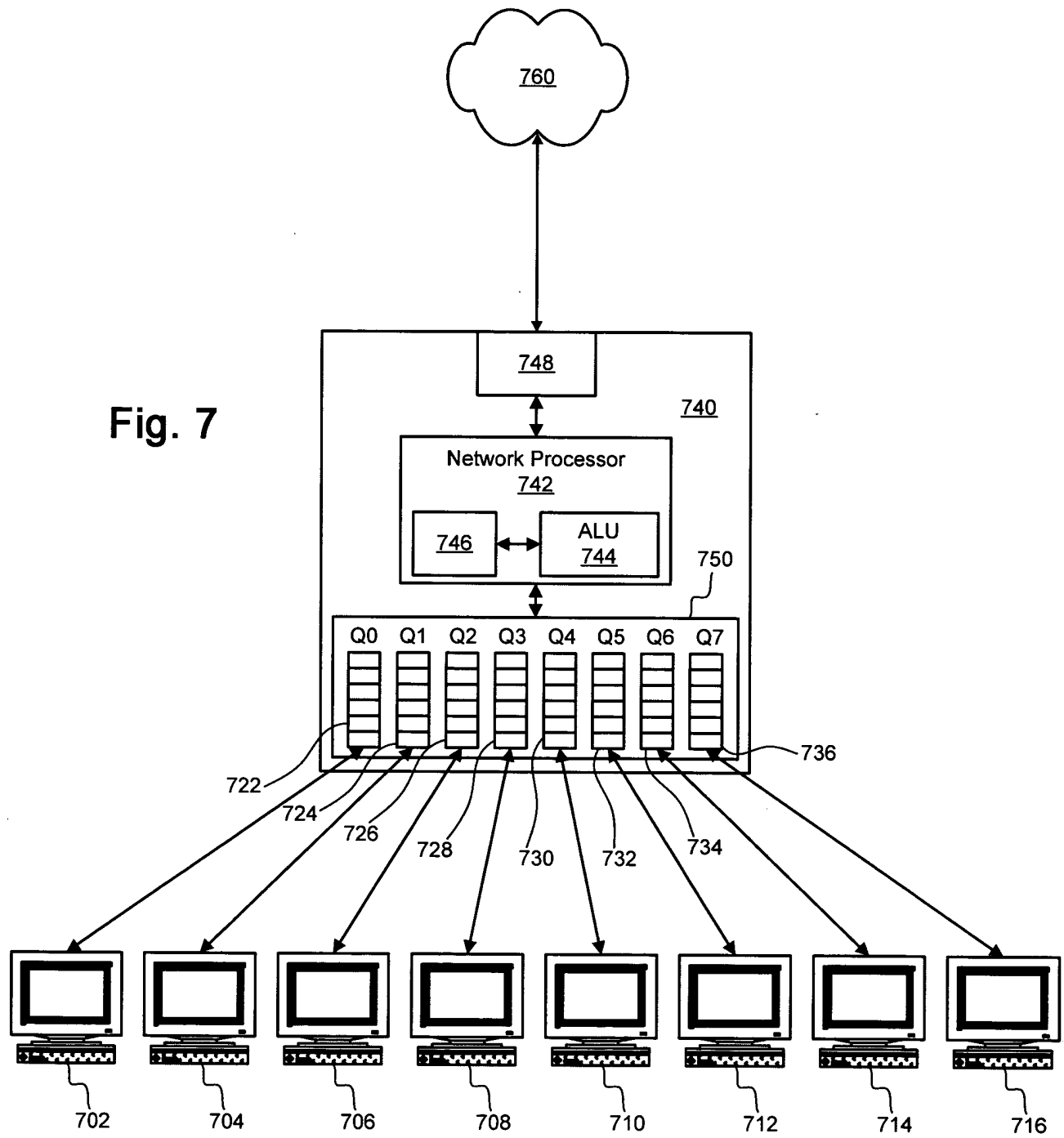


Fig. 8

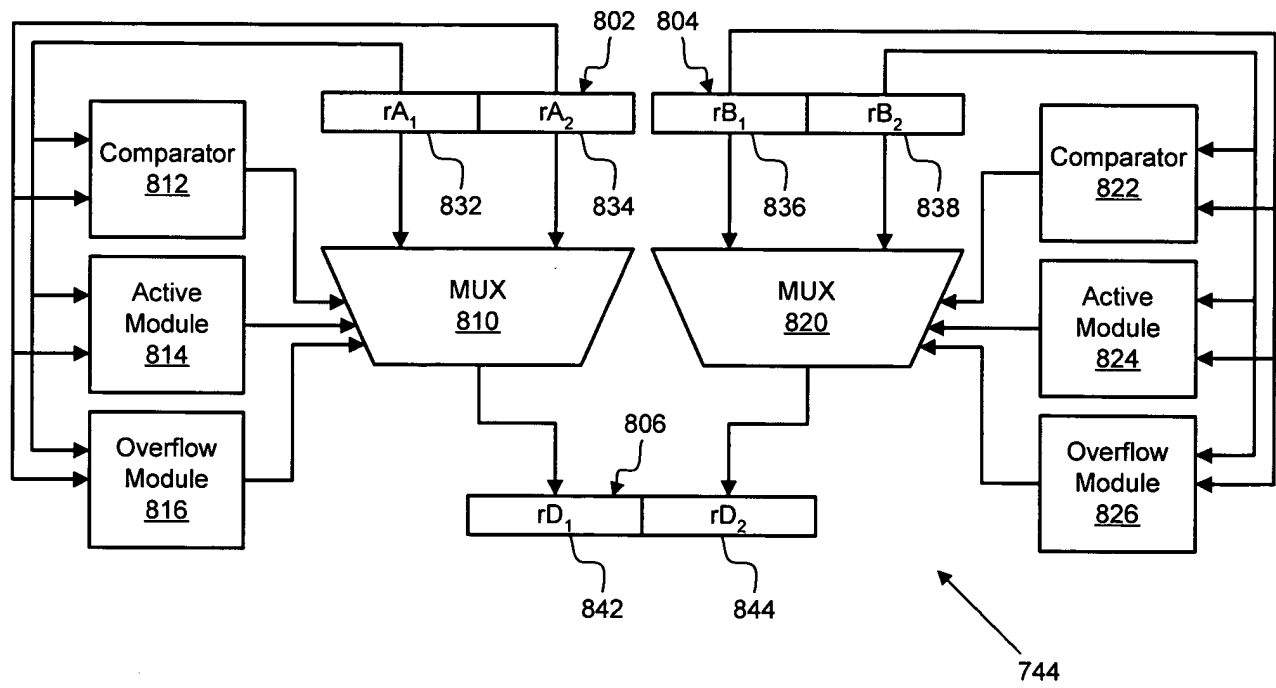




Fig. 9

